

# Hemchandracharya North Gujarat University, Patan

## B.E. SEMESTER – III (CE)

### CE304: BASIC ELECTRONICS

#### Teaching Scheme

Theory	04 Hrs/Week
Tutorial	-
Practical	02 Hrs/Week
Total	06 Hrs/Week

#### Examination Scheme

Theory	100 Marks
Practical	25 Marks
Term work	25 Marks
Total	150 Marks

1. **Energy Bands in Solids:** Charged Particles, Field Intensity, Potential Energy, The eV Unit of Energy, The Nature of the Atom, Atomic Energy Levels, Electronic Structure of The Elements, The Energy Band Theory of Crystals, Insulators, Semiconductors and Metals
2. **Transport Phenomena in Semiconductors:** Mobility and Conductivity, Electrons and Holes in an Intrinsic Semiconductor, Donor and Acceptor Impurities, Charge Densities in a Semiconductor, Electrical Properties of Ge and Si, The Hall Effect, Conductivity Modulation, Generation and Recombination of Charges, Diffusion, The Continuity Equation, Injected Minority –Carrier Charge, The Potential Variation within a Graded Semiconductor
3. **Junction –Diode Characteristics:** Open –Circuited p-n Junction, p-n Junction as a Rectifier, Current Components in a p-n Diode, Volt-Ampere Characteristic, Temperature Dependence of the V/I Characteristic, Diode Resistance, Space Charge, Transition Capacitance, Charge -Control Description of a Diode , Diffusion Capacitance , Junction Diode Switching Times, Breakdown Diodes, Tunnel Diode, Semiconductor Photodiode, Photovoltaic Effect, Light –Emitting Diodes
4. **Diode Circuits:** Diode as a Circuit Element, Load-Line Concept, Piecewise Linear Diode Model, Clipping Circuits, Clipping at Two Independent Levels, Comparators, Sampling Gate, Rectifiers, Other Full-Wave Circuits, Capacitor Filters, Additional Diode Circuits
5. **Transistor Characteristics:** Junction Transistor, Transistor Current Components, Transistor as an Amplifier, Transistor Construction, CB Configuration, CE Configuration, CE Cutoff region, CE Saturation Region, Typical Transistor, CE Current Gain, CC Configuration, Analytical Expressions for Transistor Characteristics Maximum Voltage Rating, Phototransistor
6. **Transistor at Low Frequencies:** Graphical Analysis of the CE configuration, Two-Port Devices and the Hybrid Model, Transistor Hybrid Model, h-Parameters, Conversion Formulas for the Parameters of Three Transistor Configurations, Analysis of a Transistor Amplifier Circuit Using h Parameters, Thevenin's and Norton's Theorems and Corollaries, Emitter Follower, Comparison of Transistor Amplifier Configurations, Linear Analysis of a Transistor Circuit, Miller's Theorem and its Dual, Cascading Transistor Amplifiers, Simplified CE Hybrid Model, Simplified Calculations for the CC Config., CE Amplifier with an Emitter Resistance, High Input Resistance Transistor Circuits
7. **Transistor Biasing and Thermal Stabilization:** Operating Point, Bias Stability, Self-Bias, Stabilization against Variations in  $I_{CO}$ ,  $V_{BE}$  and  $\beta$ , General Remarks on Collector-Current Stability, Bias Compensation, Thermistor and Sensistor Compensation, Thermal Runaway, Thermal Stability
8. **Field Effect Transistors:** Junction FET, Pinch-Off Voltage, JFET Volt-Ampere Characteristics, FET Small-Signal Model, MOSFET, Digital MOSFET Circuits, Low Frequency CS and CD Amplifiers, Biasing the FET, The FET as a Voltage Variable Resistor, CS Amplifier at High Frequencies, CD Amplifier at High Frequencies
9. **Power Circuits and Systems:** Class A large Signal Amplifiers, Second Harmonic Distortion, Higher –Order Harmonic Generation, Transformer Coupled Audio Power Amplifier, Efficiency, Push-Pull Amplifiers, Class B Amplifiers, Class AB Operation, Regulated Power Supplies, Series Voltage Regulator

#### Reference Books:

1. Integrated Electronics By Jacob Millman and Christos C. Halkias, Tata McGraw Hill Publication
2. Electronics Devices by Floyd, Pearson Publication [Seventh edition]
3. Electronic Devices and Circuit Theory by Robert Boylestad and Louis Nashelsky [Ninth Edition]